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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*  
*NONE*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
 JAPAN 2002-308668 10/23/2002 *ylae*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>Examiner's Signature</i> <i>ylae</i> <i>Initials</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 5	TOTAL CLAIMS 35	INDEPENDENT CLAIMS 6
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TITLE  
 Semiconductor device comprising ESD protection circuit for protecting circuit from being destructed by electrostatic discharge

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